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Requirements of Low Power VLSI Design and Analysis of Flip-flops
Sources of Power Consumption



Condition: New. Publisher/Verlag: LAP Lambert Academic Publishing | Sources of Power Consumption | In recent years, power consumption has become a critical design concern due to the growing demand of portable applications and the increasing costs incurred and difficulties encountered in cooling and heat removal processes. Flip-flops are heavily studied circuits, as they have a large impact on both cycle time and power consumption in modern synchronous systems. In many digital VLSI designs, the clock system that includes clock distribution..

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