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IMPLÉMENTATION EN SYSTEMC TLM D'UN NOC AVEC UNE TOPOLOGIE FAT-TREE



Condition: New. Publisher/Verlag: Éditions universitaires européennes | L'augmentation du parallélisme dans les circuits impose de nouvelles contraintes sur les supports de communications dans les SOC (system on chip). Les communications par bus ne permettent pas un passage à l'échelle. L'alternative basée sur la notion de réseau intégré sur puce (NoC pour Network on Chip) permet d'une part, de disposer d'architectures de communications flexibles et performantes et d'autre part de supporter un nombre grandissant d...

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