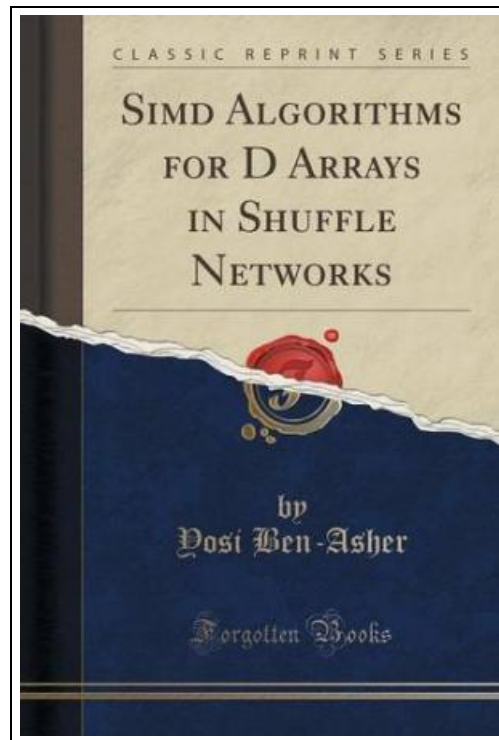


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Reviews

Simply no words to spell out. It can be rally fascinating throgh studying period of time. You will not really feel monotony at at any moment of your own time (that's what catalogues are for concerning if you ask me).
(Dr. Isabella Turner)

SIMD ALGORITHMS FOR D ARRAYS IN SHUFFLE NETWORKS (CLASSIC REPRINT)


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Forgotten Books, United States, 2015. Paperback. Book Condition: New. 229 x 152 mm. Language: English . Brand New Book ***** Print on Demand *****.Excerpt from Simd Algorithms for D Arrays in Shuffle Networks 1. Introduction and the Model Schwartz [1] introduced the idea of a parallel processor based on the shuffle connections (2); reviewed various basic of SIMD algorithms for such an ensemble of processors, and analyzed their asymptotic time complexities. However, these algorithms are designed for the 1-D case, where N , the size of the problem is restricted to be P , the number of processors. In reality, the general case where $N > P$ is much more likely to appear. In this case each processor stores $L = S/P$ data elements in its local memory. This data structure is referred to as a 2-D array of P columns and L rows, where the i th column corresponds to the local memory of the processor PE_i . Our algorithms correspond to a SIMD Perfect Shuffle connected machine (PS) [1]. For simplicity, although it is not crucial, we assume that P is a power of two. The processors are ordered left to right and are numbered $0 - P - 1$ accordingly, so we denote the i th processor PE_i ; Subgroups of processors are referred to in a natural manner: left half, right half, odd, even etc. There are three kinds of communication steps: shuffle (σ), unshuffle (σ^{-1}) and exchange (X) and a processor internal (fixed-size) computation. All of these operations take one time step. In any parallel network machine we have two possible working modes: One in which each processor computes a local result of a sequential computation using its local data. Then an inter-processor global algorithm is carried out to compute the final result. In other words, this mode involves separate sequential work by PE_i ...



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