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VHDL-Synthese : Entwurf digitaler Schaltungen und Systeme

By Jürgen Reichardt

De Oldenbourg Gruyter Mai 2015, 2015. Taschenbuch. Condition: Neu. Neuware - Die Hardwarebeschreibungssprache VHDL (Very High Speed Integrated Circuit Description Language) dient dem Entwurf der Hardwarekomponenten für komplexe Computer- und Consumer-Anwendungen. In diesem Lehrbuch wird, immer vor dem Hintergrund der Digitaltechnik, eine Einführung in Grundkonzepte aber auch detaillierter Einblick in die konkrete Synthese anhand von Beispielen gegeben. Inhaltliche Neuerungen der 7. Auflage: Durchgängige Verwendung des IEEE-Standards zur VHDL-Arithmetik Auf vielfachen Wunsch der Leser: Ergänzung um einen Abschnitt zum VHDL-Entwurf von Testbenches Ergänzung des Kapitels 'FIR-Filter' um die Modellierung systolischer FIR-Filter Erweiterung um ein neues Kapitel zur VHDL Implementierung der numerischen Integration. Dieser Abschnitt ermöglicht die Hardware-Modellierung nichtlinearer Systeme, z.B. in der Regelungstechnik. 483 pp. Deutsch.


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